

WEST Search History

DATE: Thursday, April 16, 2009

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L5	(substrate wafer semiconductor) and chamber and temperature adj (control\$3 regulat\$3 adjust\$3) and 34/\$.ccls.	219
<input type="checkbox"/>	L4	(substrate wafer semiconductor) and chamber and temperature with (control\$3 regulat\$3 adjust\$3) and 34/\$.ccls.	498
<input type="checkbox"/>	L3	(substrate wafer semiconductor) and chamber and temperature same (control\$3 regulat\$3 adjust\$3) and 34/\$.ccls.	601

END OF SEARCH HISTORY